

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

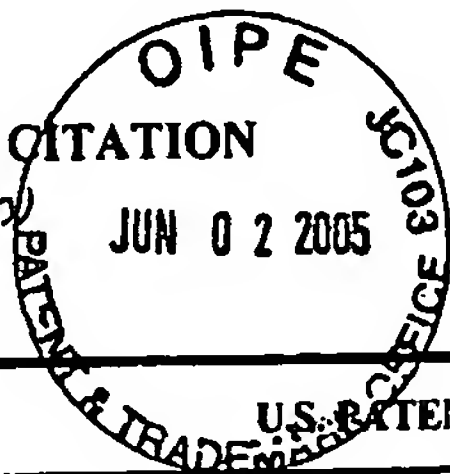
Electronic Version v18

Stylesheet Version v18.0

Title of Invention	FINFET WITH LOW GATE CAPACITANCE AND LOW EXTRINSIC RESISTANCE						
<p>Application Number : 10/711,170</p> <p>Confirmation Number:</p> <p>First Named Applicant: Brent Anderson</p> <p>Attorney Docket Number: BUR920040040US1</p> <p>Art Unit: 2818</p> <p>Examiner: HOANG-QUAN HO</p> <p>Search string: (6660596 or 6635923 or 6118161 or 20030178670 or 20030151077).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	6660596	2003-12-09	Adkisson, et al		438	286
2	2	6635923	2003-10-21	Hanafi, et al		257	327
3	3	6118161	2000-09-12	Chapman, et al		257	401
US Published Applications							
Note: Applicant is not required to submit a paper copy of cited US Published Applications							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
1	1	20030178670	2003-09-25	Fried, et al		257	315
2	2	20030151077	2003-08-14	Mathew, et al		257	250
Signature							
Examiner Name				Date			
HOANG-QUAN HO				9/15/2005			

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

BUR920040040US1

Application Number

10/711170

Applicant(s)

Anderson, et al.

Filing Date

8/30/04

Group Art Unit

Unknown

2818

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
HQA		6660596	2003-12-09	Adkisson, et al	438	286	
HQA		6635923	2003-10-21	Honafi, et al	257	327	
HQA		6118161	2000-09-12	Chapman, et al	257	401	

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
HQA		20030178670	2003-09-25	Fried, et al	257	315	
HQA		20030151077	2003-08-14	Mattew, et al	257	250	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

HQA		Lindert, et al., "Sub-60-nm Quasi-Planar FinFETs Fabricated Using a Simplified Process", IEEE Electron Device Letters, Vol. 22, No. 10, October 2001, Pages 487-489					

EXAMINER

HOANG-QUAN HO

DATE CONSIDERED

9/15/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.